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Date: October 19, 2005	Pages: 16 (including this sheet)
From: Nicole Teitler Cave	Senders Phone No.: (512) 338-6300 Senders Fax No.: (512) 338-6301

REGARDING:

Title:	LOW THRESHOLD VOLTAGE TRANSISTOR DISPLACEMENT IN A SEMICONDUCTOR DEVICE		
Application No.:	10/657,964	Filed:	September 9, 2003
Examiner:	Levin, Naum B.	Group Art Unit:	2825
Atty. Docket No.:	004-8841	Confirmation No.:	4133

ATTACHED HERETO:

- (1) Response to Non-Final Office Action (13 pages)
- (2) Transmittal Letter (2 pages)

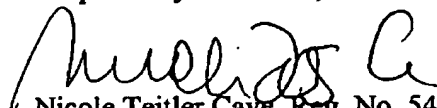
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Nicole Teitler Cave

10/19/05
Date

Respectfully submitted,


Nicole Teitler Cave, Reg. No. 54,021
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October 19, 2005

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Applicant: Sriram Satakopan et al.
 Title: LOW THRESHOLD VOLTAGE TRANSISTOR DISPLACEMENT IN A SEMICONDUCTOR DEVICE
 Application No.: 10/657,964 Filed: September 9, 2003
 Examiner: Levin, Naum B. Group Art Unit: 2825
 Atty. Docket No.: 004-8841 Conf. No.: 4133

Dear Sir:

Transmitted herewith are the following documents in the above-identified application:

- ☒ Response to Non-Final Office Action (13 page(s))
☐ Petition for Extension of Time (month) (page(s))
☐ Information Disclosure Statement (page(s)), including PTO/SB0/8A and/or PTO/SB/08B (page(s)), and copies of reference(s)
☐ Other: (page(s))
☐ Other: (page(s))
☐ Other: (page(s))
☒ Transmittal Letter (2 page(s));
☐ Return postcard;

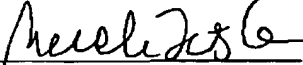
The Total Fee has been calculated as shown below:

	Pending Claims	Claims Previously Paid	Extra Claims	Fees
Total Claims	23	- 23 =	0 x \$50.00 =	0.00
Independent Claims	5	- 5 =	0 x \$200.00 =	0.00
Multiple Dependent Claims (if any) - \$360.00 fee				
Additional Claims Fee				\$.00
Fee For Extension Of Time				
Other Fees: ()				
TOTAL FEE DUE:				\$.00

- ☐ Small entity status is entitled to be asserted for the application.
☐ A check is enclosed for the Total Fee shown above.
☐ Please charge the Total Fee shown above to Deposit Account 50-0631.
☒ The Commissioner is hereby authorized to charge any deficiency in fees and any additional fees under 37 C.F.R. § 1.16 or 1.17, that may be required during the pendency of this application, and to similarly credit any overpayment, to Deposit Account 50-0631.

ZAGORIN O'BRIEN GRAHAM LLP

October 19, 2005
RE: 10/657,964
Page 2 of 2

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Respectfully submitted,



Nicole Teitler Cave, Reg. No. 54,021

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PATENT

OCT 19 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Sriram Satakopan et al.

Title: LOW THRESHOLD VOLTAGE TRANSISTOR DISPLACEMENT IN A SEMICONDUCTOR DEVICE

Application No.: 10/657,964 Filed: September 9, 2003

Examiner: Levin, Naum B. Group Art Unit: 2825

Atty. Docket No.: 004-8841 Confirmation No.: 4133

October 19, 2005

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE TO NON-FINAL OFFICE ACTION

This paper is responsive to the Non-Final Office Action mailed on July 27, 2005, having a shortened statutory period for response set to expire October 27, 2005. In light of the Amendments and/or Remarks herein, further examination is requested.